The University of Jordan
School of Engineering
Department of Computer Engineering
Fall Term – A.Y. 2025-2026

Course:	Electronics and VLSI lab – 0917462 (1 Cr. – Core Course)
Catalog Data:	The Electronics and VLSI lab is an introductory lab that introduces the students to Diode analysis and characteristics, basic MOS logic circuits, MOS voltage transfer characteristics and operation analysis, design the schematic and the layout of basic logic gates and complex combinational logic functions, delay and power analysis and optimization of combinational logic circuits, schematic of the basic sequential components like latches and Flip Flops.
Prerequisites by Course:	0907461 Electronics and VLSI design
Prerequisites by Topic:	Students are assumed to have had sufficient knowledge in NMOS and PMOS transistors operation and digital circuit design using CMOS transistors
Textbook:	1. CMOS Digital Integrated Circuits Analysis and Design.(Kang and Leblebici) 2. CMOS VLSI Design, A Circuits And Systems Perspective.(Weste and Harris)
Course Website:	MS Teams
Schedule & Duration:	14 Weeks, 8 Lab sessions, 180 minutes each
Minimum Student Material:	Text book, class handouts, some instructor keynotes, calculator and access to a personal computer and internet.
Minimum College Facilities:	Classroom with whiteboard and projection display facilities, library, and computational facilities.
Course Objectives:	The objectives of this lab are: 1. Introduce students to the design aspects of CMOS integrated circuits from device up to the register level. 2. Enable the students to use CAD tools to develop efficient circuit layouts and verify designs. Laboratory assignments include design, layout, extraction, and simulation for combinational and sequential circuits.
Course Outcomes and Relation to ABET Program Outcomes:	1. Design wide range of combinational and sequential logic blocks at the transistor level. The designed blocks should meet the delay and area constraints [2]. 2. Use CAD tools to develop efficient circuit schematics, layouts and verify designs [2,6].
Course Topics:	The lab includes ten experiments that cover the following topics: 1. CMOS transistors analysis 2. FinFET technology 3. Combinational circuit design at the schematic level 4. Combinational circuit design at the layout level 5. sequential circuit design at the schematic level 6. sequential circuit design at the layout level

Course Outline

Week	Experiment
5/10/2024	Lab Preparation
12/10/2024	Syllabus distribution + Tools Setup + **Exp1** CMOS transistors analysis using LTspice
19/10/2024	**Exp2:** CMOS inverter Design and analysis (schematic + layout)
26/10/2024	**Exp3:** Combinational Circuit Design (Basic gates) (schematic + layout)
2/11/2024	**Exp4:** Combinational circuit design (Complex Functions)(schematic + layout)
9/11/2024	**Exp5:** Combinational circuit design (pseudo NMOS, Dynamic Logic, Domino Logic)
16/11/2024	**Exp6:** Sequential Circuits Design (Latches)
23/11/2024	***Midterm Exam (Practical)***
30/11/2024	No lab
7/12/2024	**Exp7:** Sequential Circuits Design (FFs)
14/12/2024	**Exp8:** SRAM Memory Cells Design
20/12/2024	Project Announcement
28/12/2024	Project discussion
TBA	**Final Exam**

Computer Usage:

The lab will be taught on campus and the students are expected to their own laptops to solve the post lab part of the labsheets and the project. The computers available in the lab will be used for the midterm and the final exams.

Attendance:

Class attendance will be taken every class and the university's polices will be enforced in this regard.

Assessments:
Grading policy:

Labsheets	15% (10% In_Lab, 5% Post_Lab
Midterm Exam	30% Practical exam
Project	15%
Final Exam	40%

Instructors:

Eng. Abeer Awad(a.awad@ju.edu.jo)

Class Time and

Section 1: Sunday 13:30 -16:30
Section 3: Thursday 13:30 – 16:30

Location:

Parallel Processing laboratory, and Computer Design Lab

Program Outcomes (PO)

1	an ability to identify, formulate, and solve complex engineering problems by applying principles of engineering, science, and mathematics
2	an ability to apply engineering design to produce solutions that meet specified needs with consideration of public health, safety, and welfare, as well as global, cultural, social, environmental, and economic factors
3	an ability to communicate effectively with a range of audiences
4	an ability to recognize ethical and professional responsibilities in engineering situations and make informed judgments, which must consider the impact of engineering solutions in global, economic, environmental, and societal contexts
5	an ability to function effectively on a team whose members together provide leadership, create a collaborative and inclusive environment, establish goals, plan tasks, and meet objectives
6	an ability to develop and conduct appropriate experimentation, analyze and interpret data, and use engineering judgment to draw conclusions
7	an ability to acquire and apply new knowledge as needed, using appropriate learning strategies